05-22-01

CPA/2813
8/CPA

V. Vannall

CONTINUED PROSECUTION APPLICATION (CPA)
UNDER 37 C.F.R. § 1.53(d)
REQUEST TRANSMITTAL

Attorney Docket No.:	303.522US1
First Named Inventor:	Alan R. Reinberg
Express Mail No.:	EL721276019US
Total Pages (if by fax):	0

Address to:

**Commissioner for Patents
Box CPA
Washington, D.C. 20231**

This is a request for filing a continuation application under 37 CFR § 1.53(d) of prior application Serial No. 09/382,442 , filed on _August 25, 1999_ , entitled _METHOD FOR REDUCING SINGLE BIT DATA LOSS IN A MEMORY CIRCUIT_ .

The above-identified prior application in which no abandonment of, or termination of, proceedings has occurred, is hereby expressly abandoned as of the filing date of this request for a CPA. Please use all the contents of the prior application file wrapper, including the drawings, as the basic papers for the new application. (37 CFR 1.53(b) must be used for continuation-in-part applications or for applications where the prior application is not to be abandoned.)

1. __ Enter the amendment previously filed on __ under 37 CFR 1.116, but unentered, in the prior application.

2. X A Preliminary Amendment (4 pages) is enclosed.

3. __ This application is filed by fewer than all the inventors named in the prior application, 37 CFR 1.53(d)(4).

     a. __ *DELETE* the following inventor(s) named in the prior nonprovisional application:

          _____

     b. __ The inventor(s) to be deleted are set forth on a separate sheet attached hereto.

4. __ A new power of attorney is enclosed.

5. __ Information Disclosure Statement is enclosed.
     a. __ Form(s) 1449
     b. __ Copies of IDS Citations

05/25/2001 GTEFFERA 00000007 190743   09382442

01 FC:131   710.00 CH
02 FC:103   108.00 CH
03 FC:102   80.00 CH

The filing fee is calculated below on the basis of the claims existing in the prior application as amended at 1 and 2 on the previous page:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	26 - 20 =	6	x 18 =	$108.00
INDEPENDENT CLAIMS	4 - 3 =	1	x 80 =	$80.00
[ ] MULTIPLE DEPENDENT CLAIMS PRESENTED				$0.00
BASIC FEE				$710.00
TOTAL				$898.00

6. __ Small Entity Status:

     a. __ A small entity statement is enclosed.

     b. __ A small entity statement was filed in the prior nonprovisional application and such status is still proper and desired. .

     c. __ Is no longer claimed.

7. __ A check in the amount of __$898.00__ is attached to pay the filing fee.

8. _X_ **The Commissioner is hereby authorized to charge the required fee of $898.00 to Deposit Account No. 19-0743.**

9. __ A petition for extension of time in the prior application is enclosed along with a check in the amount of $0.00 to pay the extension fee.

10. _X_ Other : __A Clean Version of Pending Claims (4 pgs.)__

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: _____
Atty: Jamal M. Kalis
Reg. No. 37,650

Customer Number 21186

"Express Mail" mailing label number: __EL721276019US__
Date of Deposit: May 21, 2001
This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to Box CPA, Commissioner for Patents, Washington, D. C. 20231.

S/N 09/382,442

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alan R. Reinberg

Serial No.:   09/382,442

Filed: August 25, 1999

Title: METHOD FOR REDUCING SINGLE BIT DATA LOSS IN A MEMORY CIRCUIT

Examiner: Richard A. Booth

Group Art Unit: 2812

Docket: 303.522US1

PRELIMINARY AMENDMENT

Box CPA
Commissioner for Patents
Washington, D.C. 20231

In response to the Final Office Action mailed February 21, 2001, please amend the application as follows:

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. Specific amendments to individual claims are detailed in the following marked up set of claims.

Please amend the claims as follows:

1.   (Amended) A method for reducing random single bit data loss in a memory circuit comprising:

providing a semiconductor layer having a surface;

heating the layer in an atmosphere comprising a Hydrogen isotope; and

fabricating a memory circuit comprising single bit data using the semiconductor layer wherein single bit data loss is reduced.

3.   (Amended) The method of claim 1 and further comprising fabricating a FLASH memory circuit comprising single bit data using the semiconductor layer.

8.   (Amended) The method of claim 7 and further comprising forming a film comprising Hydrogen isotope adjacent to the gate region of the memory circuit in order to reduce single bit data loss.